

Irish Standard I.S. EN 61189-5-1:2016

Test methods for electrical materials, printed boards and other interconnection structures and assemblies - Part 5-1: General test methods for materials and assemblies -Guidance for printed board assemblies

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I.S. EN 61189-5-1:2016

Incorporating amendments/corrigenda/National Annexes issued since publication:

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NSAI T +353 1 807 3800 Sales:

 1 Swift Square,
 F +353 1 807 3838
 T +353 1 857 6730

 Northwood, Santry
 E standards@nsai.ie
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National Foreword

I.S. EN 61189-5-1:2016 is the adopted Irish version of the European Document EN 61189-5-1:2016, Test methods for electrical materials, printed boards and other interconnection structures and assemblies - Part 5-1: General test methods for materials and assemblies - Guidance for printed board assemblies

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EUROPEAN STANDARD

EN 61189-5-1

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English Version

Test methods for electrical materials, printed boards and other interconnection structures and assemblies - Part 5-1: General test methods for materials and assemblies - Guidance for printed board assemblies

(IEC 61189-5-1:2016)

Méthodes d'essai pour les matériaux électriques, les cartes imprimées et autres structures d'interconnexion et ensembles - Partie 5-1: Méthodes d'essai générales pour les matériaux et assemblages - Lignes directrices pour les assemblages de cartes à circuit imprimé (IEC 61189-5-1:2016)

Prüfverfahren für Elektromaterialien, Leiterplatten und andere Verbindungsstrukturen und Baugruppen - Teil 5-1: Allgemeine Prüfverfahren für Materialien und Baugruppen - Leitfaden für Baugruppen von Leiterplatten (IEC 61189-5-1:2016)

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This European Standard exists in three official versions (English, French, German). A version in any other language made by translation under the responsibility of a CENELEC member into its own language and notified to the CEN-CENELEC Management Centre has the same status as the official versions.

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European Committee for Electrotechnical Standardization Comité Européen de Normalisation Electrotechnique Europäisches Komitee für Elektrotechnische Normung

CEN-CENELEC Management Centre: Avenue Marnix 17, B-1000 Brussels

EN 61189-5-1:2016

European foreword

The text of document 91/1273/CDV, future edition 1 of IEC 61189-5-1, prepared by IEC/TC 91 "Electronics assembly technology" was submitted to the IEC-CENELEC parallel vote and approved by CENELEC as EN 61189-5-1:2016.

The following dates are fixed:

•	latest date by which the document has to be implemented at national level by publication of an identical national standard or by endorsement	(dop)	2017-05-09
	latest date by which the national	(dow)	2019-08-09

latest date by which the national standards conflicting with the document have to be withdrawn

(dow) 2019-08-09

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Endorsement notice

The text of the International Standard IEC 61189-5-1:2016 was approved by CENELEC as a European Standard without any modification.

In the official version, for Bibliography, the following notes have to be added for the standards indicated:

IEC 60068 (series)	NOTE	Harmonized as EN 60068 (series).
IEC 60068-1:2013	NOTE	Harmonized as EN 60068-1:2014.
IEC 60068-2-20	NOTE	Harmonized as EN 60068-2-20.
IEC 60068-2-58:2015	NOTE	Harmonized as EN 60068-2-58:2015.
IEC 61189-1	NOTE	Harmonized as EN 61189-1.
IEC 61189-5 (series)	NOTE	Harmonized as EN 61189-5 (series).
IEC 61189-5	NOTE	Harmonized as EN 61189-5.
IEC 61189-5-1:2016	NOTE	Harmonized as EN 61189-5-1:2016.
IEC 61189-5-2:2015	NOTE	Harmonized as EN 61189-5-2:2015.
IEC 61189-5-3:2015	NOTE	Harmonized as EN 61189-5-3:2015.
IEC 61189-5-4:2015	NOTE	Harmonized as EN 61189-5-4:2015.
IEC 61189-6	NOTE	Harmonized as EN 61189-6.
IEC 61190-1-1	NOTE	Harmonized as EN 61190-1-1.
IEC 61190-1-2	NOTE	Harmonized as EN 61190-1-2.
IEC 61190-1-3	NOTE	Harmonized as EN 61190-1-3.



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