



NSAI
Standards

Irish Standard
I.S. EN 61189-2-719:2016

Test methods for electrical materials, printed boards and other interconnection structures and assemblies - Part 2-719: Test methods for materials for interconnection structures - Relative permittivity and loss tangent (500 MHz to 10 GHz)

I.S. EN 61189-2-719:2016

Incorporating amendments/corrigenda/National Annexes issued since publication:

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I.S. xxx: Irish Standard — national specification based on the consensus of an expert panel and subject to public consultation.

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This document replaces/revises/consolidates the NSAI adoption of the document(s) indicated on the CEN/CENELEC cover/Foreword and the following National document(s):

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This document is based on:

EN 61189-2-719:2016

Published:

2016-10-14

*This document was published
under the authority of the NSAI
and comes into effect on:*

2016-11-01

ICS number:

31.180

NOTE: If blank see CEN/CENELEC cover page

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Údarás um Chaighdeáin Náisiúnta na hÉireann

National Foreword

I.S. EN 61189-2-719:2016 is the adopted Irish version of the European Document EN 61189-2-719:2016, Test methods for electrical materials, printed boards and other interconnection structures and assemblies - Part 2-719: Test methods for materials for interconnection structures - Relative permittivity and loss tangent (500 MHz to 10 GHz)

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EUROPEAN STANDARD

EN 61189-2-719

NORME EUROPÉENNE

EUROPÄISCHE NORM

October 2016

ICS 31.180

English Version

**Test methods for electrical materials, printed boards and other
interconnection structures and assemblies -
Part 2-719: Test methods for materials for interconnection
structures - Relative permittivity and loss tangent (500 MHz to 10
GHz)
(IEC 61189-2-719:2016)**

Méthode d'essai pour les matériaux électriques, les cartes
imprimées et autres structures d'interconnexion et
ensembles - Partie 2-719: Méthodes d'essai des matériaux
pour structures d'interconnexion - Permittivité relative et
tangente de perte (500 MHz à 10 GHz)
(IEC 61189-2-719:2016)

Prüfverfahren für Elektromaterialien, Leiterplatten und
andere Verbindungsstrukturen und Baugruppen -
Teil 2-719: Prüfverfahren für Materialien von
Verbindungsstrukturen - Relative Permittivität und
Verlustfaktor (500 MHz bis 10 GHz)
(IEC 61189-2-719:2016)

This European Standard was approved by CENELEC on 2016-08-16. CENELEC members are bound to comply with the CEN/CENELEC Internal Regulations which stipulate the conditions for giving this European Standard the status of a national standard without any alteration.

Up-to-date lists and bibliographical references concerning such national standards may be obtained on application to the CEN-CENELEC Management Centre or to any CENELEC member.

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European Committee for Electrotechnical Standardization
Comité Européen de Normalisation Electrotechnique
Europäisches Komitee für Elektrotechnische Normung

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EN 61189-2-719:2016

European foreword

The text of document 91/1366/FDIS, future edition 1 of IEC 61189-2-719, prepared by IEC/TC 91 "Electronics assembly technology" was submitted to the IEC-CENELEC parallel vote and approved by CENELEC as EN 61189-2-719:2016.

The following dates are fixed:

- latest date by which the document has to be (dop) 2017-05-16
implemented at national level by
publication of an identical national
standard or by endorsement
- latest date by which the national (dow) 2019-08-16
standards conflicting with the
document have to be withdrawn

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