



**NSAI**  
Standards

Irish Standard  
I.S. EN 62433-2:2017

# EMC IC modelling - Part 2: Models of integrated circuits for EMI behavioural simulation - Conducted emissions modelling (ICEM-CE)

**I.S. EN 62433-2:2017**

*Incorporating amendments/corrigenda/National Annexes issued since publication:*

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## National Foreword

I.S. EN 62433-2:2017 is the adopted Irish version of the European Document EN 62433-2:2017, EMC IC modelling - Part 2: Models of integrated circuits for EMI behavioural simulation - Conducted emissions modelling (ICEM-CE)

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EUROPEAN STANDARD

**EN 62433-2**

NORME EUROPÉENNE

EUROPÄISCHE NORM

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ICS 31.200; 33.100.10

Supersedes EN 62433-2:2010

English Version

**EMC IC modelling -  
Part 2: Models of integrated circuits for EMI behavioural  
simulation - Conducted emissions modelling (ICEM-CE)  
(IEC 62433-2:2017)**

Modèles de circuits intégrés pour la CEM - Partie 2:  
Modèles de circuits intégrés pour la simulation du  
comportement lors de perturbations électromagnétiques -  
Modélisation des émissions conduites (ICEM-CE)  
(IEC 62433-2:2017)

EMV-IC-Modellierung - Teil 2: Modelle integrierter  
Schaltungen für die Simulation des Verhaltens bei  
elektromagnetischer Beeinflussung - Modellierung  
leitungsgeführter Aussendungen (ICEM-CE)  
(IEC 62433-2:2017)

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Up-to-date lists and bibliographical references concerning such national standards may be obtained on application to the CEN-CENELEC Management Centre or to any CENELEC member.

This European Standard exists in three official versions (English, French, German). A version in any other language made by translation under the responsibility of a CENELEC member into its own language and notified to the CEN-CENELEC Management Centre has the same status as the official versions.

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European Committee for Electrotechnical Standardization  
Comité Européen de Normalisation Electrotechnique  
Europäisches Komitee für Elektrotechnische Normung

**CEN-CENELEC Management Centre: Avenue Marnix 17, B-1000 Brussels**

## **EN 62433-2:2017**

### **European foreword**

The text of document 47A/999/FDIS, future edition 2 of IEC 62433-2, prepared by SC 47A "Integrated circuits", of IEC/TC 47 "Semiconductor devices" was submitted to the IEC-CENELEC parallel vote and approved by CENELEC as EN 62433-2:2017.

The following dates are fixed:

- latest date by which the document has to be implemented at national level by publication of an identical national standard or by endorsement (dop) 2017-12-03
- latest date by which the national standards conflicting with the document have to be withdrawn (dow) 2020-03-03

This document supersedes EN 62433-2:2010.

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In the official version, for Bibliography, the following notes have to be added for the standards indicated:

IEC 61967	NOTE	Harmonized in EN 61967 series.
IEC 61967-4	NOTE	Harmonized as EN 61967-4.

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