



**NSAI**  
Standards

Irish Standard  
I.S. EN IEC 62228-1:2018

# Integrated circuits - EMC evaluation of transceivers - Part 1: General conditions and definitions

**I.S. EN IEC 62228-1:2018**

*Incorporating amendments/corrigenda/National Annexes issued since publication:*

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## National Foreword

I.S. EN IEC 62228-1:2018 is the adopted Irish version of the European Document EN IEC 62228-1:2018, Integrated circuits - EMC evaluation of transceivers - Part 1: General conditions and definitions

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EUROPEAN STANDARD

**EN IEC 62228-1**

NORME EUROPÉENNE

EUROPÄISCHE NORM

June 2018

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ICS 31.200

English Version

**Integrated circuits - EMC evaluation of transceivers - Part 1:  
General conditions and definitions  
(IEC 62228-1:2018)**

Circuits intégrés - Évaluation CEM des émetteurs-  
récepteurs - Partie 1 : Conditions générales et définitions  
(IEC 62228-1:2018)

Integrierte Schaltungen - Bewertung der  
elektromagnetischen Verträglichkeit von Sende-  
Empfangsgeräten - Teil 1: Allgemeine Bedingungen und  
Festlegungen  
(IEC 62228-1:2018)

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European Committee for Electrotechnical Standardization  
Comité Européen de Normalisation Electrotechnique  
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## **EN IEC 62228-1:2018 (E)**

### **European foreword**

The text of document 47A/1018/CDV, future edition 1 of IEC 62228-1, prepared by IEC/SC 47A "Integrated circuits" of IEC/TC 47 "Semiconductor devices" was submitted to the IEC-CENELEC parallel vote and approved by CENELEC as EN IEC 62228-1:2018.

The following dates are fixed:

- latest date by which the document has to be implemented at national level by publication of an identical national standard or by endorsement (dop) 2018-12-01
- latest date by which the national standards conflicting with the document have to be withdrawn (dow) 2021-06-01

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