This is a free page sample. Access the full version online.

AS/NZS 1102.112:1995

IEC 617-12:1991 IEC 617-12:1991/Amd.1:1992 IEC 617-12:1991/Amd.2:1994

Australian/New Zealand Standard

Graphical symbols for electrotechnology

Part 112: Binary logic elements

[IEC title: Graphical symbols for diagrams, Part 12: Binary logic elements]

AS/NZS 1102.112:1995

This Joint Australian/New Zealand Standard was prepared by Joint Technical Committee TE/13, Symbols Units and Quantities for Electrotechnology. It was approved on behalf of the Council of Standards Australia on 4 October 1994 and on behalf of the Council of Standards New Zealand on 17 October 1994. It was published on 5 January 1995.

The following interests are represented on Committee TE/13:

Association of Consulting Engineers Australia
Austel
Australian Chamber of Commerce and Industry
Civil Aviation Authority
Department of Defence
Department of Technical and Further Education, S.A.
Institution of Engineers Australia
Institution of Radio and Electronics Engineers Australia
Queensland Mining Council
Railways of Australia
Royal Melbourne Institute of Technology
State Training Board, Vic.

Review of Standards. To keep abreast of progress in industry, Joint Australian/New Zealand Standards are subject to periodic review and are kept up to date by the issue of amendments or new editions as necessary. It is important therefore that Standards users ensure that they are in possession of the latest edition, and any amendments thereto.

Full details of all Joint Standards and related publications will be found in the Standards Australia and Standards New Zealand Catalogue of Publications; this information is supplemented each month by the magazines 'The Australian Standard' and 'Standards New Zealand', which subscribing members receive, and which give details of new publications, new editions and amendments, and of withdrawn Standards.

Suggestions for improvements to Joint Standards, addressed to the head office of either Standards Australia or Standards New Zealand, are welcomed. Notification of any inaccuracy or ambiguity found in a Joint Australian/New Zealand Standard should be made without delay in order that the matter may be investigated and appropriate action taken.

© Copyright - STANDARDS AUSTRALIA

Users of Standards are reminded that copyright subsists in all Standards Australia publications and software. Except where the Copyright Act allows and except where provided for below no publications or software produced by Standards Australia may be reproduced, stored in a retrieval system in any form or transmitted by any means without prior permission in writing from Standards Australia. Permission may be conditional on an appropriate royalty payment. Requests for permission and information on commercial software royalties should be directed to the head office of Standards Australia.

Standards Australia will permit up to 10 percent of the technical content pages of a Standard to be copied for use exclusively inhouse by purchasers of the Standard without payment of a royalty or advice to Standards Australia.

Standards Australia will also permit the inclusion of its copyright material in computer software programs for no royalty payment provided such programs are used exclusively in-house by the creators of the programs.

Care should be taken to ensure that material used is from the current edition of the Standard and that it is updated whenever the Standard is amended or revised. The number and date of the Standard should therefore be clearly identified.

The use of material in print form or in computer software programs to be used commercially, with or without payment, or in commercial contracts is subject to the payment of a royalty. This policy may be varied by Standards Australia at any time.

AS/NZS 1102.112:1995

Australian/New Zealand Standard

Graphical symbols for electrotechnology

Part 112: Binary logic elements

First published as AS 1102.9—1971. Second edition 1979. Third edition 1986.

Jointly revised and redesignated as Joint Standard AS/NZS 1102.112:1995.

PUBLISHED JOINTLY BY:

STANDARDS AUSTRALIA 1 The Crescent, Homebush NSW 2140 Australia

STANDARDS NEW ZEALAND Level 10, Standards House, 155 The Terrace, Wellington 6001 New Zealand ii

PREFACE

This Standard was prepared by the Joint Standards Australia/ Standards New Zealand Committee TE/13 on Symbols, Units and Quantities for Electrotechnology. It is issued as a Joint Standard.

The objective of this Standard is to provide users of electrotechnical documents with graphical symbols on binary logic elements to obtain consistent presentation and meaning in diagrams.

This Standard is identical with and has been reproduced from IEC 617-12:1991, *Graphical symbols for diagrams*, Part 12: *Binary logic elements* and includes Amendment 1 to that Standard.

As this Standard is reproduced from an international Standard, the following applies:

- (a) The international Standard number does not appear on each page of text and its identity is shown only on the cover.
- (b) In the source text, 'this International Standard' should read 'this Australian/New Zealand Standard'.
- (c) A full point substitutes for a comma when referring to a decimal marker.

The symbol numbers in this Standard have retained the IEC 617-12 numbers. In AS 1102, *Graphical symbols for electrotechnology*, Part 101: *General information and general index*, the symbol numbers have '112' as the first part of the symbol number instead of '12'. References in this Standard to symbols in which the first part of the symbol number is '01' to '10' should be substituted by '101' to '110' respectively.

The equivalent Australian Standard to IEC 117-15 (superseded) referred to below is AS 1102.9—1979.

References to international Standards should be replaced by equivalent Australian or Australian/New Zealand Standards, as follows:

Reference to International Standard IEC		Australian or Australian/New Zealand Standard AS		
113-7	Diagrams, charts and tables, Part 7: Preparation of logic diagrams	1103	Diagrams, charts and tables for electrotechnology	
117-15	Recommended Graphical Symbols, Part 15: Binary logic elements (Superseded)	1103.8	Part 8: Guiding principles for the preparation of logic diagrams	
	,	1102	Graphical symbols for electrotechnology	
617-2	Graphic symbols for diagrams, Part 2: Symbol elements, qualifying symbols and other symbols having	1102.9	Part 9: Binary logic elements	
	general application	1102.102	Symbol elements, qualifying symbols and other symbols having general	
617-3	Graphical symbols for diagrams,		application	
	Part 3: conductors and connecting devices	1102.103	Part 103: Conductors and connecting devices	
617-13	Graphical symbols for diagrams Part 13: Analogue elements	AS/NZS 1102.113	Part 113: Analogue elements	

iii

CONTENTS

			Page
		CHAPTER I: GENERAL	
Section	1	Introduction	1
Section	2	General notes	1
Section	3	Explanation of terms	2
		CHAPTER II: SYMBOL CONSTRUCTION	
Section	4	Composition of the symbol	3
Section	5	Outlines	
Section	6	Use and combination of outlines	
		CHAPTER III: QUALIFYING SYMBOLS ASSOCIATED WITH INPUTS, OUTPUTS, AND OTHER CONNECTIONS	
Section	7	Negation, logic polarity and dynamic input	10
Section	8	Internal connections	12
Section	9	Symbols inside the outline	14
Section	10	Non-logic connections and signal-flow indicators	40
		CHAPTER IV: DEPENDENCY NOTATION	
Section	11	General explanation	42
Section	12	Convention	42
Section	13	Types of dependency	43
Section	14	AND dependency	
Section	15	OR dependency	48
Section	16	NEGATE dependency	49
Section	17	INTERCONNECTION dependency	50
Section	17A	TRANSMISSION dependency	51
Section	18	CONTROL dependency	
Section	19	SET and RESET dependency	54
Section	20	ENABLE dependency	
Section	21	MODE dependency	58
Section	22	Comparison of C-, EN- and M-effects on inputs	60
Section	23	ADDRESS dependency	61
Section	24	Special techniques used in dependency notation	65
Section	25	The ordering of labels associated with inputs and with outputs	67



	This is a free preview.	Purchase the e	entire publication	at the link below:
--	-------------------------	----------------	--------------------	--------------------

Product Page

- Dooking for additional Standards? Visit Intertek Inform Infostore
- Dearn about LexConnect, All Jurisdictions, Standards referenced in Australian legislation