



National Standards Authority of Ireland

IRISH STANDARD

I.S. EN 190102:1994

ICS 31.200

**TTL SCHOTTKY DIGITAL INTEGRATED  
CIRCUITS – SERIES 54S, 64S, 74S, 84S  
(FAMILY SPECIFICATION)**

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Údarás um Chaighdeáin Náisiúnta na hÉireann



**DECLARATION**

OF

SPECIFICATION

ENTITLED

TTL SCHOTTKY DIGITAL INTEGRATED CIRCUITS – SERIES 54S, 64S, 74S, 84S

(FAMILY SPECIFICATION)

AS

THE IRISH STANDARD SPECIFICATION FOR

TTL SCHOTTKY DIGITAL INTEGRATED CIRCUITS – SERIES 54S, 64S, 74S, 84S

(FAMILY SPECIFICATION)

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Forfás in exercise of the power conferred by section 20 (5) of the Industrial Research and Standards Act, 1961 ( No. 20 of 1961 ) and the Industrial Development Act, 1993 (No. 19 of 1993), and with the consent of the Minister for Enterprise and Employment, hereby declares as follows:

1. This instrument may be cited as the Standard Specification (TTL Schottky Digital Integrated Circuits – Series 54S, 64S, 74S, 84S (Family Specification)) Declaration, 1994.
2. (1) The Specification set forth in the Schedule to this declaration is hereby declared to be the standard specification for TTL Schottky Digital Integrated Circuits – Series 54S, 64S, 74S, 84S (Family Specification). The Schedule comprises the text of EN 190102 : 1994.  
  
(2) The said standard specification may be cited as Irish Standard/EN 190102:1994 or as I.S./EN 190102:1994.
3. (1) The Standard Specification (TTL Schottky Digital Integrated Circuits – Series 54S, 64S, 74S, 84S (Family Specification)) Declaration, 1991, is hereby revoked.  
  
(2) Reference in any other standard specification to the Instrument hereby revoked and to Irish Standard 557:1991 (CECC 90102:1989) thereby prescribed, shall be construed, respectively, as references to this Instrument and to Irish Standard/EN 190102:1994.



**EUROPEAN STANDARD**  
**NORME EUROPÉENNE**  
**EUROPÄISCHE NORM**

**EN 190 102**

May 1994

UDC

Supersedes CECC 90 102 Issue 2 : 1989

Descriptors: Quality, electronic components, TTL Schottky digital integrated circuits

English version

**Family Specification:**

**TTL Schottky Digital Integrated Circuits**  
**Series 54S, 64S, 74S, 84S**

**Spécification de famille:**  
**Circuits intégrés logiques**  
**TTL Schottky**  
**Séries 54S, 64S, 74S, 84S**

**Familienspezifikation:**  
**Digitale integrierte TTL-**  
**Schottky-Schaltungen**  
**Serien 54S, 64S, 74S, 84S**

This European Standard was approved by the CENELEC Electronic Components Committee (CECC) on 30 April 1994. CENELEC members are bound to comply with CEN/CENELEC Internal Regulations which stipulate the conditions for giving this European Standard the status of a national standard without any alteration.

Up-to-date lists and bibliographical references concerning such national standards may be obtained on application to the General Secretariat of the CECC or to any CENELEC member.

This European Standard exists in three official versions (English, French, German). A version in any other language made by translation under the responsibility of a CENELEC member into its own language and notified to the CECC General Secretariat has the same status as the official versions.

CENELEC members are the national electrotechnical committees of Austria, Belgium, Denmark, Finland, France, Germany, Greece, Iceland, Ireland, Italy, Luxembourg, Netherlands, Norway, Portugal, Spain, Sweden, Switzerland, and United Kingdom. The membership of the CECC is identical, with the exception of the national electrotechnical committees of Greece, Iceland and Luxembourg.

**CECC**

CENELEC Electronic Components Committee  
Comité des Composants Electroniques du CENELEC  
CENELEC- Komitee für Bauelemente der Elektronik  
**General Secretariat: Gartenstr. 179, D-60596 Frankfurt am Main**

## FOREWORD

The CENELEC Electronic Components Committee (CECC) is composed of those member countries of the European Committee for Electrotechnical Standardization (CENELEC) who wish to take part in a harmonized System for electronic components of assessed quality.

The object of the System is to facilitate international trade by the harmonization of the specifications and quality assessment procedures for electronic components, and by the grant of an internationally recognized Mark, or Certificate, of Conformity. The components produced under the System are thereby acceptable in all member countries without further testing.

This European Standard was prepared by CECC WG 9, 'Integrated Circuits'.

The text of the draft based on document CECC 90 102 Issue 2 : 1989 (with A1) was submitted to the formal vote for conversion to a European Standard; together with the voting report, circulated as document CECC(Secretariat)3535 it was approved by CECC as EN 190 102 on 30 April 1994.

The following dates were fixed:

- |   |       |                   |
|---|-------|-------------------|
| - latest date of announcement of the EN at national level       | (doa) | <b>1994-09-01</b> |
| - latest date of publication of an identical national standard* | (dop) | <b>1995-03-01</b> |
| - latest date of withdrawal of conflicting national standards*  | (dow) | <b>1996-03-01</b> |

\* National Standard (excluding National implementation of IECQ Specifications)

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