

DECLARATION

OF

SPECIFICATION

ENTITLED

TRANSMISSION AND MULTIPLEXING (TM); PHYSICAL AND ELECTRICAL
CHARACTERISTICS OF HIERARCHICAL DIGITAL INTERFACES FOR EQUIPMENT
USING THE 2048 KBIT/S – BASED PLESIOCHRONOUS OR
SYNCHRONOUS DIGITAL HIERARCHIES

AS

THE IRISH STANDARD SPECIFICATION FOR

TRANSMISSION AND MULTIPLEXING (TM); PHYSICAL AND ELECTRICAL
CHARACTERISTICS OF HIERARCHICAL DIGITAL INTERFACES FOR EQUIPMENT
USING THE 2048 KBIT/S – BASED PLESIOCHRONOUS OR
SYNCHRONOUS DIGITAL HIERARCHIES

EOLAS - The Irish Science and Technology Agency in exercise of the power conferred by section 20 (3) of the Industrial Research and Standards Act, 1961 (No. 20 of 1961) and the Science and Technology Act, 1987 (No. 30 of 1987), and with the consent of the Minister for Enterprise and Employment, hereby declares as follows:

1. This instrument may be cited as the Standard Specification (Transmission and Multiplexing (TM); Physical and Electrical Characteristics of Hierarchical Digital Interfaces for Equipment Using the 2048 kbit/s – Based Plesiochronous or Synchronous Digital Hierarchies) Declaration, 1993.
2. (1) The Specification set forth in the Schedule to this declaration is hereby declared to be the standard specification for Transmission and Multiplexing (TM); Physical and Electrical Characteristics of Hierarchical Digital Interfaces for Equipment Using the 2048 kbit/s – Based Plesiochronous or Synchronous Digital Hierarchies. The Schedule comprises the text of ETS 300166 : 1993.

(2) The said standard specification may be cited as Irish Standard/ETS 300166:1993 or as I.S./ETS 300166:1993.



I.S./ETS 300166:1993

IRISH STANDARD

**TRANSMISSION AND MULTIPLEXING (TM); PHYSICAL
AND ELECTRICAL CHARACTERISTICS OF
HIERARCHICAL DIGITAL INTERFACES FOR
EQUIPMENT USING THE 2048 KBIT/S – BASED
PLESIOCHRONOUS OR SYNCHRONOUS DIGITAL
HIERARCHIES**

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STANDARD**ETS 300 166**

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**Transmission and Multiplexing (TM);
Physical and electrical characteristics of hierarchical digital
interfaces for equipment using the 2 048 kbit/s -
based plesiochronous or synchronous digital hierarchies**

ETSI

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