



National Standards Authority of Ireland

IRISH STANDARD

I.S. EN 60191-6-5:2002

ICS 31.080.01

**MECHANICAL STANDARDIZATION OF
SEMICONDUCTOR DEVICES
PART 6-5: GENERAL RULES FOR THE
PREPARATION OF OUTLINE DRAWINGS OF
SURFACE MOUNTED SEMICONDUCTOR
DEVICE PACKAGES -
DESIGN GUIDE FOR FINE-PITCH BALL GRID
ARRAY (FBGA)
(IEC 60191-6-5:2001)**

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EUROPEAN STANDARD

EN 60191-6-5

NORME EUROPÉENNE

EUROPÄISCHE NORM

October 2001

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English version

Mechanical standardization of semiconductor devices
Part 6-5: General rules for the preparation of outline drawings of
surface mounted semiconductor device packages -
Design guide for fine-pitch ball grid array (FBGA)
(IEC 60191-6-5:2001)

Normalisation mécanique des dispositifs
à semiconducteurs
Partie 6-5: Règles générales pour
la préparation des dessins
d'encombrement des dispositifs à
semiconducteurs à montage en surface -
Guide de conception pour les boîtiers
matriciels à billes et à pas fins (FBGA)
(CEI 60191-6-5:2001)

Mechanische Normung von
Halbleiterbauelemente
Teil 6-5: Allgemeine Regeln für
die Erstellung von Gehäusezeichnungen
von SMD-Halbleitergehäusen -
Konstruktionsleitfaden für Feinraster-
Ball-Grid-Arrays (FBGA)
(IEC 60191-6-5:2001)

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Up-to-date lists and bibliographical references concerning such national standards may be obtained on application to the Central Secretariat or to any CENELEC member.

This European Standard exists in three official versions (English, French, German). A version in any other language made by translation under the responsibility of a CENELEC member into its own language and notified to the Central Secretariat has the same status as the official versions.

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CENELEC

European Committee for Electrotechnical Standardization
Comité Européen de Normalisation Electrotechnique
Europäisches Komitee für Elektrotechnische Normung

Central Secretariat: rue de Stassart 35, B - 1050 Brussels

Foreword

The text of document 47D/437/FDIS, future edition 1 of IEC 60191-6-5, prepared by SC 47D, Mechanical standardization of semiconductor devices, of IEC TC 47, Semiconductor devices, was submitted to the IEC-CENELEC parallel vote and was approved by CENELEC as EN 60191-6-5 on 2001-10-01.

The following dates were fixed:

- latest date by which the EN has to be implemented
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national standard or by endorsement (dop) 2002-07-01
- latest date by which the national standards conflicting
with the EN have to be withdrawn (dow) 2004-10-01

Annexes designated "normative" are part of the body of the standard.
In this standard, annex ZA is normative.
Annex ZA has been added by CENELEC.

Endorsement notice

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