



National Standards Authority of Ireland  
Údarás um Chaighdeáin Náisiúnta na hÉireann

IRISH STANDARD

**I.S. EN 61188-5-8:2008**

ICS 31.180

**PRINTED BOARDS AND PRINTED BOARD  
ASSEMBLIES - DESIGN AND USE -- PART 5  
-8: ATTACHMENT (LAND/JOINT)  
CONSIDERATIONS - AREA ARRAY  
COMPONENTS (BGA, FBGA, CGA, LGA)  
(IEC 61188-5-8:2007 (EQV))**

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EUROPEAN STANDARD  
NORME EUROPÉENNE  
EUROPÄISCHE NORM

**EN 61188-5-8**

March 2008

ICS 31.180

English version

**Printed boards and printed board assemblies -  
Design and use -  
Part 5-8: Attachment (land/joint) considerations -  
Area array components (BGA, FBGA, CGA, LGA)  
(IEC 61188-5-8:2007)**

Cartes imprimées  
et cartes imprimées équipées -  
Conception et utilisation -  
Partie 5-8: Considérations sur les liaisons  
pistes-soudures -  
Composants matriciels  
(BGA, FBGA, CGA, LGA)  
(CEI 61188-5-8:2007)

Leiterplatten und Flachbaugruppen -  
Konstruktion und Anwendung -  
Teil 5-8: Betrachtungen zur Montage  
(Anschlussfläche/Verbindung) -  
Flächenmatrix-Bauelemente  
(BGA, FBGA, CGA, LGA)  
(IEC 61188-5-8:2007)

This European Standard was approved by CENELEC on 2008-02-01. CENELEC members are bound to comply with the CEN/CENELEC Internal Regulations which stipulate the conditions for giving this European Standard the status of a national standard without any alteration.

Up-to-date lists and bibliographical references concerning such national standards may be obtained on application to the Central Secretariat or to any CENELEC member.

This European Standard exists in three official versions (English, French, German). A version in any other language made by translation under the responsibility of a CENELEC member into its own language and notified to the Central Secretariat has the same status as the official versions.

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**CENELEC**

European Committee for Electrotechnical Standardization  
Comité Européen de Normalisation Electrotechnique  
Europäisches Komitee für Elektrotechnische Normung

**Central Secretariat: rue de Stassart 35, B - 1050 Brussels**

## Foreword

The text of document 91/705/FDIS, future edition 1 of IEC 61188-5-8, prepared by IEC TC 91, Electronics assembly technology, was submitted to the IEC-CENELEC parallel vote and was approved by CENELEC as EN 61188-5-8 on 2008-02-01.

This European Standard is to be read in conjunction with EN 61188-5-1.

The following dates were fixed:

- |  |       |            |
|--|-------|------------|
| – latest date by which the EN has to be implemented<br>at national level by publication of an identical<br>national standard or by endorsement | (dop) | 2008-11-01 |
| – latest date by which the national standards conflicting<br>with the EN have to be withdrawn  | (dow) | 2011-02-01 |

Annex ZA has been added by CENELEC.

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## Endorsement notice

The text of the International Standard IEC 61188-5-8:2007 was approved by CENELEC as a European Standard without any modification.

In the official version, for Bibliography, the following note has to be added for the standard indicated:

IEC 61191-1	NOTE	Harmonized as EN 61191-1:1998 (not modified).
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## Annex ZA (normative)

### Normative references to international publications with their corresponding European publications

The following referenced documents are indispensable for the application of this document. For dated references, only the edition cited applies. For undated references, the latest edition of the referenced document (including any amendments) applies.

NOTE When an international publication has been modified by common modifications, indicated by (mod), the relevant EN/HD applies.

<u>Publication</u>	<u>Year</u>	<u>Title</u>	<u>EN/HD</u>	<u>Year</u>
IEC 60068-2-58	– <sup>1)</sup>	Environmental testing - Part 2-58: Tests - Test Td: Test methods for solderability, resistance to dissolution of metallization and to soldering heat of surface mounting devices (SMD)	EN 60068-2-58 + corr. December	2004 <sup>2)</sup> 2004
IEC 60191-2	Series	Mechanical standardization of semiconductor devices - Part 2: Dimensions	–	–
IEC 61188-5-1	– <sup>1)</sup>	Printed boards and printed board assemblies - Design and use - Part 5-1: Attachment (land/joint) considerations - Generic requirements	EN 61188-5-1	2002 <sup>2)</sup>
IEC 62090	– <sup>1)</sup>	Product package labels for electronic components using bar code and two- dimensional symbologies	EN 62090	2003 <sup>2)</sup>

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<sup>1)</sup> Undated reference.

<sup>2)</sup> Valid edition at date of issue.



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