



National Standards Authority of Ireland
Údarás um Chaighdeáin Náisiúnta na hÉireann

IRISH STANDARD

I.S. EN 62132-3:2007

ICS 31.200

**INTEGRATED CIRCUITS - MEASUREMENT
OF ELECTROMAGNETIC IMMUNITY, 150
KHZ TO 1 GHZ -- PART 3: BULK CURRENT
INJECTION (BCI) METHOD (IEC 62132
-3:2007 (EQV))**

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I.S. EN 62132-3:2007

EUROPEAN STANDARD

EN 62132-3

NORME EUROPÉENNE

EUROPÄISCHE NORM

October 2007

ICS 31.200

English version

**Integrated circuits -
Measurement of electromagnetic immunity, 150 kHz to 1 GHz -
Part 3: Bulk current injection (BCI) method
(IEC 62132-3:2007)**

Circuits intégrés -
Mesure de l'immunité électromagnétique,
150 kHz à 1 GHz -
Partie 3: Méthode d'injection
de courant (BCI)
(CEI 62132-3:2007)

Integrierte Schaltungen -
Messung der elektromagnetischen
Störfestigkeit im Frequenzbereich
von 150 kHz bis 1 GHz -
Teil 3: Stromeinspeisungs-
(BCI-)Verfahren
(IEC 62132-3:2007)

This European Standard was approved by CENELEC on 2007-10-01. CENELEC members are bound to comply with the CEN/CENELEC Internal Regulations which stipulate the conditions for giving this European Standard the status of a national standard without any alteration.

Up-to-date lists and bibliographical references concerning such national standards may be obtained on application to the Central Secretariat or to any CENELEC member.

This European Standard exists in three official versions (English, French, German). A version in any other language made by translation under the responsibility of a CENELEC member into its own language and notified to the Central Secretariat has the same status as the official versions.

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CENELEC

European Committee for Electrotechnical Standardization
Comité Européen de Normalisation Electrotechnique
Europäisches Komitee für Elektrotechnische Normung

Central Secretariat: rue de Stassart 35, B - 1050 Brussels

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Foreword

The text of document 47A/773/FDIS, future edition 1 of IEC 62132-3, prepared by SC 47A, Integrated circuits, of IEC TC 47, Semiconductor devices, was submitted to the IEC-CENELEC parallel vote and was approved by CENELEC as EN 62132-3 on 2007-10-01.

The following dates were fixed:

- | | | |
|--|-------|------------|
| – latest date by which the EN has to be implemented
at national level by publication of an identical
national standard or by endorsement | (dop) | 2008-07-01 |
| – latest date by which the national standards conflicting
with the EN have to be withdrawn | (dow) | 2010-10-01 |

Annex ZA has been added by CENELEC.

Endorsement notice

The text of the International Standard IEC 62132-3:2007 was approved by CENELEC as a European Standard without any modification.

Annex ZA
(normative)

**Normative references to international publications
with their corresponding European publications**

The following referenced documents are indispensable for the application of this document. For dated references, only the edition cited applies. For undated references, the latest edition of the referenced document (including any amendments) applies.

NOTE When an international publication has been modified by common modifications, indicated by (mod), the relevant EN/HD applies.

<u>Publication</u>	<u>Year</u>	<u>Title</u>	<u>EN/HD</u>	<u>Year</u>
IEC 62132-1	2006	Integrated circuits - Measurement of electromagnetic immunity, 150 kHz to 1 GHz - Part 1: General conditions and definitions	EN 62132-1 + corr. November	2006 2006



IEC 62132-3

Edition 1.0 2007-09

INTERNATIONAL STANDARD

NORME INTERNATIONALE

Integrated circuits – Measurement of electromagnetic immunity, 150 kHz to 1 GHz –

Part 3: Bulk current injection (BCI) method

**Circuits intégrés – Mesure de l'immunité électromagnétique, 150 kHz à 1 GHz –
Partie 3: Méthode d'injection de courant (BCI)**

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