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Irish Standard Recommendation
S.R. CWA 16374-33:2014

Extensions for Financial Services (XFS) interface specification – Release 3.20 – Part 33: XFS MIB Device Specific Definitions – PIN Keypad Device Class MIB 3.20

S.R. CWA 16374-33:2014

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AGREEMENT

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**Extensions for Financial Services (XFS) interface specification -
Release 3.20 - Part 33: XFS MIB Device Specific Definitions -
PIN Keypad Device Class MIB 3.20**

This CEN Workshop Agreement has been drafted and approved by a Workshop of representatives of interested parties, the constitution of which is indicated in the foreword of this Workshop Agreement.

The formal process followed by the Workshop in the development of this Workshop Agreement has been endorsed by the National Members of CEN but neither the National Members of CEN nor the CEN-CENELEC Management Centre can be held accountable for the technical content of this CEN Workshop Agreement or possible conflicts with standards or legislation.

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Foreword

This CWA is revision 3.20 of the XFS interface specification.

This CEN Workshop Agreement has been drafted and approved by a Workshop of representatives of interested parties on 2011-06-29, the constitution of which was supported by CEN following the public call for participation made on 1998-06-24. The specification is continuously reviewed and commented in the CEN/ISSS Workshop on XFS. It is therefore expected that an update of the specification will be published in due time as a CWA, superseding this revision 3.20.

A list of the individuals and organizations which supported the technical consensus represented by the CEN Workshop Agreement is available to purchasers from the CEN-CENELEC Management Centre. These organizations were drawn from the banking sector. The CEN/ISSS XFS Workshop gathered suppliers as well as banks and other financial service companies.

The CWA is published as a multi-part document, consisting of:

Part 1: Application Programming Interface (API) - Service Provider Interface (SPI) - Programmer's Reference

Part 2: Service Class Definition - Programmer's Reference

Part 3: Printer and Scanning Device Class Interface - Programmer's Reference

Part 4: Identification Card Device Class Interface - Programmer's Reference

Part 5: Cash Dispenser Device Class Interface - Programmer's Reference

Part 6: PIN Keypad Device Class Interface - Programmer's Reference

Part 7: Check Reader/Scanner Device Class Interface - Programmer's Reference

Part 8: Depository Device Class Interface - Programmer's Reference

Part 9: Text Terminal Unit Device Class Interface - Programmer's Reference

Part 10: Sensors and Indicators Unit Device Class Interface - Programmer's Reference

Part 11: Vendor Dependent Mode Device Class Interface - Programmer's Reference

Part 12: Camera Device Class Interface - Programmer's Reference

Part 13: Alarm Device Class Interface - Programmer's Reference

Part 14: Card Embossing Unit Class Interface - Programmer's Reference

Part 15: Cash-In Module Device Class Interface - Programmer's Reference

Part 16: Card Dispenser Device Class Interface - Programmer's Reference

Part 17: Barcode Reader Device Class Interface - Programmer's Reference

Part 18: Item Processing Module Device Class Interface - Programmer's Reference

Parts 19 - 28: Reserved for future use.

Parts 29 through 47 constitute an optional addendum to this CWA. They define the integration between the SNMP standard and the set of status and statistical information exported by the Service Providers.

Part 29: XFS MIB Architecture and SNMP Extensions MIB 3.20

Part 30: XFS MIB Device Specific Definitions - Printer Device Class MIB 3.20

Part 31: XFS MIB Device Specific Definitions - Identification Card Device Class MIB 3.20

Part 32: XFS MIB Device Specific Definitions - Cash Dispenser Device Class MIB 3.20

Part 33: XFS MIB Device Specific Definitions - PIN Keypad Device Class MIB 3.20

Part 34: XFS MIB Device Specific Definitions - Check Reader/Scanner Device Class MIB 3.20

Part 35: XFS MIB Device Specific Definitions - Depository Device Class MIB 3.20

Part 36: XFS MIB Device Specific Definitions - Text Terminal Unit Device Class MIB 3.20

Part 37: XFS MIB Device Specific Definitions - Sensors and Indicators Unit Device Class MIB 3.20

Part 38: XFS MIB Device Specific Definitions - Camera Device Class MIB 3.20

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Part 39: XFS MIB Device Specific Definitions - Alarm Device Class MIB 3.20

Part 40: XFS MIB Device Specific Definitions - Card Embossing Unit Class MIB 3.20

Part 41: XFS MIB Device Specific Definitions - Cash-In Module Device Class MIB 3.20

Part 42: Reserved for future use.

Part 43: XFS MIB Device Specific Definitions - Vendor Dependent Mode Class MIB 3.20

Part 44: XFS MIB Application Management MIB 3.20

Part 45: XFS MIB Device Specific Definitions - Card Dispenser Device Class MIB 3.20

Part 46: XFS MIB Device Specific Definitions - Barcode Reader Device Class MIB 3.20

Part 47: XFS MIB Device Specific Definitions - Item Processing Module Device Class MIB 3.20

Parts 48 - 60 are reserved for future use.

Part 61: Application Programming Interface (API) - Service Provider Interface (SPI) - Migration from Version 3.10 (CWA 15748) to Version 3.20 (this CWA) - Programmer's Reference

Part 62: Printer and Scanning Device Class Interface Migration from Version 3.10 (CWA 15748) to Version 3.20 (this CWA) - Programmer's Reference

Part 63: Identification Card Device Class Interface - Migration from Version 3.10 (CWA 15748) to Version 3.20 (this CWA) - Programmer's Reference

Part 64: Cash Dispenser Device Class Interface - Migration from Version 3.10 (CWA 15748) to Version 3.20 (this CWA) - Programmer's Reference

Part 65: PIN Keypad Device Class Interface - Migration from Version 3.10 (CWA 15748) to Version 3.20 (this CWA) - Programmer's Reference

Part 66: Check Reader/Scanner Device Class Interface - Migration from Version 3.10 (CWA 15748) to Version 3.20 (this CWA) - Programmer's Reference

Part 67: Depository Device Class Interface - Migration from Version 3.10 (CWA 15748) to Version 3.20 (this CWA) - Programmer's Reference

Part 68: Text Terminal Unit Device Class Interface - Migration from Version 3.10 (CWA 15748) to Version 3.20 (this CWA) - Programmer's Reference

Part 69: Sensors and Indicators Unit Device Class Interface - Migration from Version 3.10 (CWA 15748) to Version 3.20 (this CWA) - Programmer's Reference

Part 70: Vendor Dependent Mode Device Class Interface - Migration from Version 3.10 (CWA 15748) to Version 3.20 (this CWA) - Programmer's Reference

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Part 72: Alarm Device Class Interface - Migration from Version 3.10 (CWA 15748) to Version 3.20 (this CWA) - Programmer's Reference

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Part 76: Barcode Reader Device Class Interface - Migration from Version 3.10 (CWA 15748) to Version 3.20 (this CWA) - Programmer's Reference

Part 77: Item Processing Module Device Class Interface - Migration from Version 3.10 (CWA 15748) to Version 3.20 (this CWA) - Programmer's Reference

In addition to these Programmer's Reference specifications, the reader of this CWA is also referred to a complementary document, called Release Notes. The Release Notes contain clarifications and explanations on the CWA specifications, which are not requiring functional changes. The current version of the Release Notes is available online from <http://www.cen.eu/cen/Sectors/Sectors/ISSS/Activity/Pages/WSXFS.aspx>.

The information in this document represents the Workshop's current views on the issues discussed as of the date of publication. It is furnished for informational purposes only and is subject to change without notice. CEN/ISSS makes no warranty, express or implied, with respect to this document.

The final review/endorsement round for parts 29-47 of this CWA was started on 2014-06-23 and was successfully closed on 2014-07-23. The final text for parts 29-47 of this CWA was submitted to CEN for publication on 2014-08-22.

This CEN Workshop Agreement is publicly available as a reference document from the National Members of The following countries: Austria, Belgium, Bulgaria, Croatia, Cyprus, Czech Republic, Denmark, Estonia, Finland, Former Yugoslav Republic of Macedonia, France, Germany, Greece, Hungary, Iceland, Ireland, Italy, Latvia, Lithuania, Luxembourg, Malta, Netherlands, Norway, Poland, Portugal, Romania, Slovakia, Slovenia, Spain, Sweden, Switzerland, Turkey and the United Kingdom.

Comments or suggestions from the users of the CEN Workshop Agreement are welcome and should be addressed to the CEN-CENELEC Management Centre.

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Revision History:

1.0	January 20, 2004	Initial release of XFS MIB specification.
1.10	April 15, 2007	Update of the MIB to add support for a Detailed Status Trap, a Device Reset capability and the support of SMIV2.
3.10	December 14, 2010	Update of the MIB to add support for a Capabilities table and to align the MIB with XFS 3.10.
3.20	March 28, 2014	Update release to align the MIB with XFS 3.20.

1 Introduction

This document provides the device specific MIB definition (Management Information Base) variables for the xfsPIN sub-tree version one, as foreseen by the *XFS MIB Architecture and SNMP Extensions Programmer's reference* document. All the attributes in all the MIBs are Mandatory. In the case where a vendor's device does not support an attribute then a request for this unsupported attribute should return NULL.

The xfsPIN version one sub-tree is identified by:

xfsMIBRoot

- xfsManagedService (2)
 - xfsPIN (4)
 - xfsPINV1 (1)

The xfsPINV1 sub-tree contains the following variables:

- * *xfsPINInstances(1)* is the number of managed services for the PIN class installed on the XFS subsystem. It is a 32 bit numerical field.
- * *xfsPINStatusTable(2)* identifies the table for the PIN variables.
- * *xfsPINSubDeviceTable(3)* not applicable to the PIN device.
- * *xfsPINErrorTable(4)* identifies the table for the PIN error counters.
- * *xfsPINResetTable(5)* identifies the table for the PIN reset variable.
- * *xfsPINResetDeviceTable(6)* identifies the table for the PIN reset device variables.
- * *xfsPINCcapabilitiesTable(7)* identifies the table for the PIN capabilities variables.

The *XFS MIB Architecture and SNMP Extensions Programmer's Reference* document provides an overview of the MIB structure. The following picture shows the structure of the *xfsPINV1* sub-tree.

This is a free preview. Purchase the entire publication at the link below:

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