Irish Standard
I.S. EN 61189-5-3:2015

Test methods for electrical materials, printed boards and other interconnection structures and assemblies - Part 5-3: General test methods for materials and assemblies - Soldering paste for printed board assemblies

I.S. EN 61189-5-3:2015

Incorporating amendments/corrigenda/National Annexes issued since publication:

The National Standards Authority of Ireland (NSAI) produces the following categories of formal documents:

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SWiFT xxx: A rapidly developed recommendatory document based on the consensus of the participants of an NSAI workshop.

This document replaces/revises/consolidates the NSAI adoption of the document(s) indicated on the CEN/CENELEC cover/Foreword and the following National document(s):

NOTE: The date of any NSAI previous adoption may not match the date of its original CEN/CENELEC document.

This document is based on:	*Published:*
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Údarás um Chaighdeáin Náisiúnta na hÉireann

EUROPEAN STANDARD

NORME EUROPÉENNE

EUROPÄISCHE NORM

EN 61189-5-3

March 2015

ICS 31.180

English Version

Test methods for electrical materials, printed boards and other interconnection structures and assemblies - Part 5-3: General test methods for materials and assemblies - Soldering paste for printed board assemblies (IEC 61189-5-3:2015)

Méthodes d'essai pour les matériaux électriques, les cartes imprimées et autres structures d'interconnexion et ensembles - Partie 5-3: Méthodes d'essai générales pour les matériaux et les assemblages - Pâte de brasage pour les assemblages de cartes imprimées (IEC 61189-5-3:2015)

Prüfverfahren für Elektromaterialien, Leiterplatten und andere Verbindungsstrukturen und Baugruppen - Teil 5-3: Allgemeine Prüfverfahren für Materialien und Baugruppen - Lotpaste für bestückte Leiterplatten (IEC 61189-5-3:2015)

This European Standard was approved by CENELEC on 2015-02-12. CENELEC members are bound to comply with the CEN/CENELEC Internal Regulations which stipulate the conditions for giving this European Standard the status of a national standard without any alteration.

Up-to-date lists and bibliographical references concerning such national standards may be obtained on application to the CEN-CENELEC Management Centre or to any CENELEC member.

This European Standard exists in three official versions (English, French, German). A version in any other language made by translation under the responsibility of a CENELEC member into its own language and notified to the CEN-CENELEC Management Centre has the same status as the official versions.

CENELEC members are the national electrotechnical committees of Austria, Belgium, Bulgaria, Croatia, Cyprus, the Czech Republic, Denmark, Estonia, Finland, Former Yugoslav Republic of Macedonia, France, Germany, Greece, Hungary, Iceland, Ireland, Italy, Latvia, Lithuania, Luxembourg, Malta, the Netherlands, Norway, Poland, Portugal, Romania, Slovakia, Slovenia, Spain, Sweden, Switzerland, Turkey and the United Kingdom.

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Comité Européen de Normalisation Electrotechnique
Europäisches Komitee für Elektrotechnische Normung

CEN-CENELEC Management Centre: Avenue Marnix 17, B-1000 Brussels

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Foreword

The text of document 91/1211/FDIS, future edition 1 of IEC 61189-5-3, prepared by IEC/TC 91 "Electronics assembly technology" was submitted to the IEC-CENELEC parallel vote and approved by CENELEC as EN 61189-5-3:2015.

The following dates are fixed:

• latest date by which the document has to be implemented at national level by publication of an identical national standard or by endorsement	(dop)	2015-11-12
• latest date by which the national standards conflicting with the document have to be withdrawn	(dow)	2018-02-12

Attention is drawn to the possibility that some of the elements of this document may be the subject of patent rights. CENELEC [and/or CEN] shall not be held responsible for identifying any or all such patent rights.

Endorsement notice

The text of the International Standard IEC 61189-5-3:2015 was approved by CENELEC as a European Standard without any modification.

In the official version, for Bibliography, the following notes have to be added for the standards indicated:

IEC 60068-1:2013	NOTE	Harmonized as EN 60068-1:2014 (not modified).
IEC 60068-2-20	NOTE	Harmonized as EN 60068-2-20.
IEC 61189-1	NOTE	Harmonized as EN 61189-1.
IEC 61189-2	NOTE	Harmonized as EN 61189-2.
IEC 61189-3	NOTE	Harmonized as EN 61189-3.
IEC 61190-1-1	NOTE	Harmonized as EN 61190-1-1.
IEC 61249-2-7	NOTE	Harmonized as EN 61249-2-7.
IEC 62137:2004	NOTE	Harmonized as EN 62137:2004 (not modified).
ISO 9001	NOTE	Harmonized as EN ISO 9001.
ISO 9455-2	NOTE	Harmonized as EN ISO 9455-2.

Annex ZA
(normative)

Normative references to international publications with their corresponding European publications

The following documents, in whole or in part, are normatively referenced in this document and are indispensable for its application. For dated references, only the edition cited applies. For undated references, the latest edition of the referenced document (including any amendments) applies.

NOTE 1 When an International Publication has been modified by common modifications, indicated by (mod), the relevant EN/HD applies.

NOTE 2 Up-to-date information on the latest versions of the European Standards listed in this annex is available here: www.cenelec.eu

Publication	Year	Title	EN/HD	Year
IEC 61189-5	-	Test methods for electrical materials, interconnection structures and assemblies - Part 5: Test methods for printed board assemblies	EN 61189-5	-
IEC 61189-6	-	Test methods for electrical materials, interconnection structures and assemblies - Part 6: Test methods for materials used in manufacturing electronic assemblies	EN 61189-6	-
IEC 61190-1-2	2014	Attachment materials for electronic assembly - Part 1-2: Requirements for soldering pastes for high-quality interconnects in electronics assembly	EN 61190-1-2	2014
IEC 61190-1-3	-	Attachment materials for electronic assembly - Part 1-3: Requirements for electronic grade solder alloys and fluxed and non-fluxed solid solders for electronic soldering applications	EN 61190-1-3	-

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IEC 61189-5-3

Edition 1.0 2015-01

INTERNATIONAL STANDARD

NORME INTERNATIONALE

colour inside

Test methods for electrical materials, printed boards and other interconnection structures and assemblies – Part 5-3: General test methods for materials and assemblies – Soldering paste for printed board assemblies

Méthodes d'essai pour les matériaux électriques, les cartes imprimées et autres structures d'interconnexion et ensembles – Partie 5-3: Méthodes d'essai générales pour les matériaux et les assemblages – Pâte de brasage pour les assemblages de cartes imprimées

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CONTENTS

INTERNATIONAL ELECTROTECHNICAL COMMISSION

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TEST METHODS FOR ELECTRICAL MATERIALS, PRINTED BOARDS AND OTHER INTERCONNECTION STRUCTURES AND ASSEMBLIES –

Part 5-3: General test methods for materials and assemblies – Soldering paste for printed board assemblies

FOREWORD

1) The International Electrotechnical Commission (IEC) is a worldwide organization for standardization comprising all national electrotechnical committees (IEC National Committees). The object of IEC is to promote international co-operation on all questions concerning standardization in the electrical and electronic fields. To this end and in addition to other activities, IEC publishes International Standards, Technical Specifications, Technical Reports, Publicly Available Specifications (PAS) and Guides (hereafter referred to as "IEC Publication(s)"). Their preparation is entrusted to technical committees; any IEC National Committee interested in the subject dealt with may participate in this preparatory work. International, governmental and non-governmental organizations liaising with the IEC also participate in this preparation. IEC collaborates closely with the International Organization for Standardization (ISO) in accordance with conditions determined by agreement between the two organizations.
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International Standard IEC 61189-5-3 has been prepared by IEC technical committee 91: Electronics assembly technology.

The text of this standard is based on the following documents:

FDIS	Report on voting
91/1211/FDIS	91/1224/RVD

Full information on the voting for the approval of this standard can be found in the report on voting indicated in the above table.

This publication has been drafted in accordance with the ISO/IEC Directives, Part 2.

This International Standard is used in conjunction with IEC 61189-1:1997, IEC 61189-2:2006, IEC 61189-3:2007.

A list of all parts in the IEC 61189 series, published under the general title *Test methods for electrical materials, printed boards and other interconnection structures and assemblies*, can be found on the IEC website.

The committee has decided that the contents of this publication will remain unchanged until the stability date indicated on the IEC web site under "http://webstore.iec.ch" in the data related to the specific publication. At this date, the publication will be

- reconfirmed,
- withdrawn,
- replaced by a revised edition, or
- amended.

IMPORTANT – The 'colour inside' logo on the cover page of this publication indicates that it contains colours which are considered to be useful for the correct understanding of its contents. Users should therefore print this document using a colour printer.

INTRODUCTION

IEC 61189 relates to test methods for materials or component robustness for printed board assemblies, irrespective of their method of manufacture.

The standard is divided into separate parts, covering information for the designer and the test methodology engineer or technician. Each part has a specific focus; methods are grouped according to their application and numbered sequentially as they are developed and released.

In some instances test methods developed by other TCs (for example, TC 104) have been reproduced from existing IEC standards in order to provide the reader with a comprehensive set of test methods. When this situation occurs, it will be noted on the specific test method; if the test method is reproduced with minor revision, those paragraphs that are different are identified.

This part of IEC 61189 contains test methods for evaluating robustness of materials or components for printed board assemblies. The methods are self-contained, with sufficient detail and description so as to achieve uniformity and reproducibility in the procedures and test methodologies.

The tests shown in this standard are grouped according to the following principles:

P: preparation/conditioning methods

V: visual test methods

D: dimensional test methods

C: chemical test methods

M: mechanical test methods

E: electrical test methods

N: environmental test methods

X: miscellaneous test methods

To facilitate reference to the tests, to retain consistency of presentation, and to provide for future expansion, each test is identified by a number (assigned sequentially) added to the prefix (group code) letter showing the group to which the test method belongs.

The test method numbers have no significance with respect to an eventual test sequence; that responsibility rests with the relevant specification that calls for the method being performed. The relevant specification, in most instances, also describes pass/fail criteria.

The letter and number combinations are for reference purposes to be used by the relevant specification. Thus "5-3X01" represents the first chemical test method described in IEC 61189-5-3.

In short, in this example, 5-3 is the number of the part of IEC 61189, X is the group of methods, and 01 is the test number.

TEST METHODS FOR ELECTRICAL MATERIALS, PRINTED BOARDS AND OTHER INTERCONNECTION STRUCTURES AND ASSEMBLIES –

Part 5-3: General test methods for materials and assemblies – Soldering paste for printed board assemblies

1 Scope

This part of IEC 61189 is a catalogue of test methods representing methodologies and procedures that can be applied to test printed board assemblies.

This part of IEC 61189 focuses on test methods for soldering paste based on the existing IEC 61189-5 and IEC 61189-6. In addition, it includes test methods of soldering paste for lead free soldering.

2 Normative references

The following documents, in whole or in part, are normatively referenced in this document and are indispensable for its application. For dated references, only the edition cited applies. For undated references, the latest edition of the referenced document (including any amendments) applies.

IEC 61189-5, *Test methods for electrical materials, interconnection structures and assemblies – Part 5: Test methods for printed board assemblies*

IEC 61189-6, *Test methods for electrical materials, interconnection structures and assemblies – Part 6: Test methods for materials used in manufacturing electronic assemblies*

IEC 61190-1-2:2014, *Attachment materials for electronic assembly – Part 1-2: Requirements for soldering pastes for high-quality interconnections in electronics assembly*

IEC 61190-1-3, *Attachment materials for electronic assembly – Part 1-3: Requirements for electronic grade solder alloys and fluxed and non-fluxed solid solders for electronic soldering applications*

3 Accuracy, precision and resolution

3.1 General

Errors and uncertainties are inherent in all measurement processes. The information given below enables valid estimates of the amount of error and uncertainty to be taken into account.

Test data serve a number of purposes which include

– monitoring of a process;
– enhancing of confidence in quality conformance;
– arbitration between customer and supplier.

In any of these circumstances, it is essential that confidence can be placed upon the test data in terms of